

APPLICATION DATA SHEET

APPLICATION INFORMATION

Application number::	{For use with Supplemental data
Filing Date::	sheets (only after initial filing)}
Application Type::	Regular
Subject Matter::	Utility
Suggested classification:	None
Suggested Group Art Unit::	2812
CD-ROM or CD-R?::	None
Number of CD disks::	
Number of copies of CDs::	
Sequence submission?::	None
Computer Readable form (CRF)?::	
Number of copies of CRF::	
Title::	Method of Fabricating A High-Voltage Transistor With A Multi-Layered Extended Drain Structure
Attorney Docket Number::	03692.P059DC
Request for Early Publication?::	No
Request for Non-Publication?::	No
Suggested Drawing Figure::	None
Total Drawing Sheets::	9
Small Entity?::	No
Latin name::	None
Variety denomination name::	None
Petition included?::	No
Petition Type::	
Licensed US Govt. Agency::	No
Contract or Grant Numbers::	No
Secrecy Order in Parent Appl.?::	No

APPLICANT INFORMATION

Applicant Authority Type::	Inventor
Primary Citizenship Country::	US
Status::	Full Capacity
Given Name::	Donald
Middle Name::	Ray
Family Name::	Disney
Name Suffix::	
City of Residence::	Cupertino
State or Province of Residence::	CA
Country of Residence::	US
Street of mailing address::	10153 Colby Avenue

City of mailing address:: Cupertino
State or Province of mailing address:: CA
Country of mailing address:: US
Postal or Zip Code of mailing address:: 95014

CORRESPONDENCE INFORMATION

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DOMESTIC PRIORITY INFORMATION

Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application	Continuation of	10/278,551	10/22/02
Which is a	Division of	09/948,879	09/07/01

REPRESENTATIVE INFORMATION

Representative Customer Number::	27660	
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ASSIGNMENT INFORMATION

Assignee name:: Power Integrations, Inc.
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City of mailing address:: San Jose
State or Province of mailing address:: CA
Country of mailing address:: US
Postal or Zip Code of mailing address:: 95138